

LT8775 --- Product Brief

MIPI to 2-Port LVDS Converter

1. Features

● Dual-Port LVDS Transmitter

- Compatible with VESA and JEIDA standard
- 1~2 configurable port
- 1 clock lane and 5 data lanes per port
- Two port simultaneous display supported
- Resolution up to 3840x2160 30Hz or any other resolution with pixel clock between 6.25MHz to 297MHz
- Support DE and SYNC mode
- Support YCbCr4:2:2
- Programmable pre-emphasis
- Support output SSC(30KHz±5%)

● MIPI Receiver

- Compliant with DCS1.02, D-PHY1.2, DSI1.2 and CSI-2 1.00
- 1 clock lane and 1~4 configurable data lanes
- 80Mb/s ~ 2.5Gb/s per data lane
- Resolution up to 3840x2160 30Hz or any other resolution with pixel clock between 6.25MHz to 297MHz
- Both non-burst and burst video mode supported
- Support RGB666, loosely RGB666, RGB888, RGB565,

16-bit YCbCr4:2:2 video format

● Miscellaneous

- Support 100KHz and 400KHz I2C slave
- External 25MHz ± 50ppm crystal reference clock is preferred

2. General Description

LT8775 is a high performance MIPI DSI/CSI-2 to Dual-Port LVDS convertor. LT8775 deserializes input MIPI video data, decodes packets, and converts the formatted video data stream to LVDS transmitter output between AP and mobile display panel or camera.

LT8775 support maximum 12.5dB input equalization and programmable pre-emphasis to improve performance.

3. Applications

- Mobile systems
- Cellular handsets
- Digital video cameras
- Digital still cameras
- Tablet PC, Notebook PC
- Car Display and Camera System

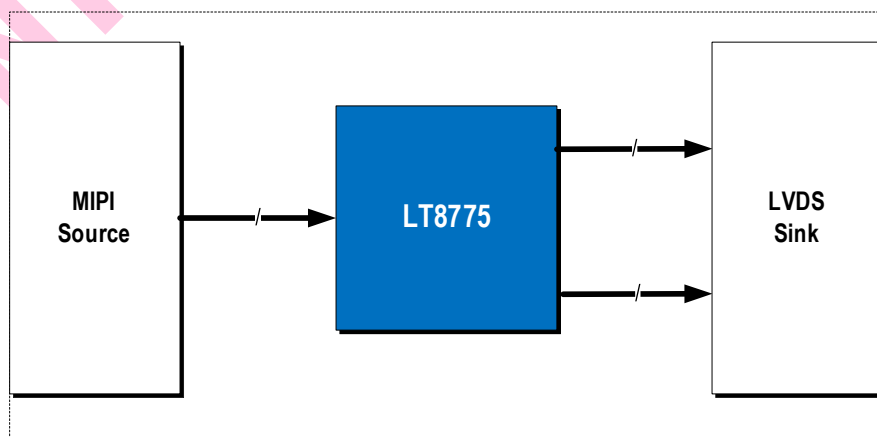


Figure 3.1 Typical Application Diagram

4. Ordering Information

Table 4.1 Ordering Information

Product Name	Part Number	Product Status	Package	Bonding Wire	Grade	Operating Temperature Range	Stack Die Option	Packing Method	MPQ
LT8775	LT8775_U2Q07CEN	Preview	QFN64 (7.5*7.5) Saw	Cu	E	-40°C to +85°C	N	Tray	2600pcs

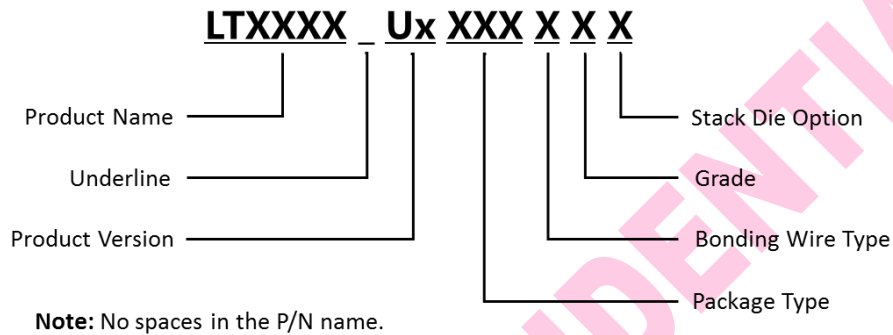


Figure 4.1 Part Number Naming Rules

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